

Servo analog +5V

Servo MOTOR +5V

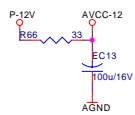
System +5V

Video buffer +5V

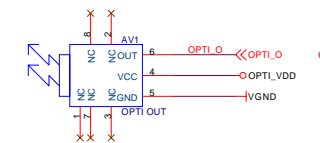
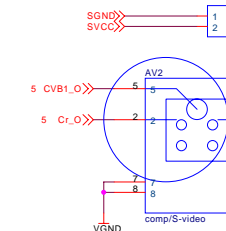
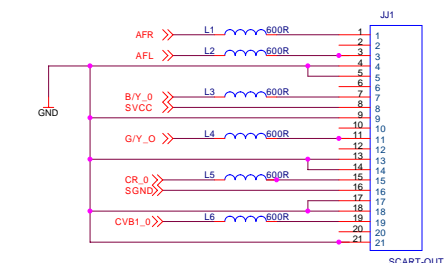
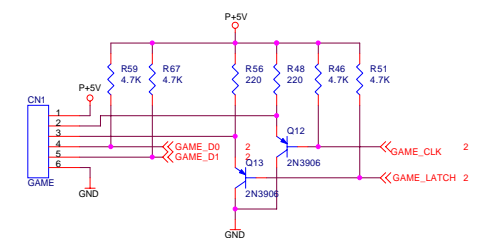
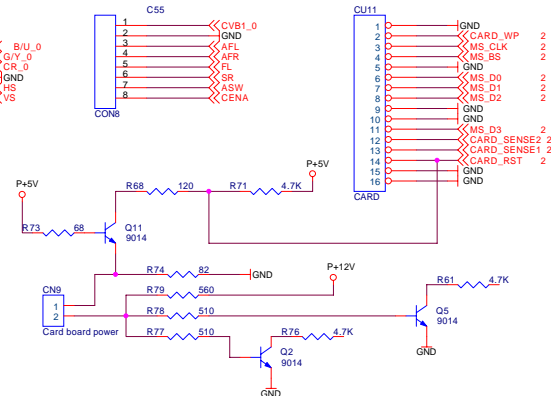
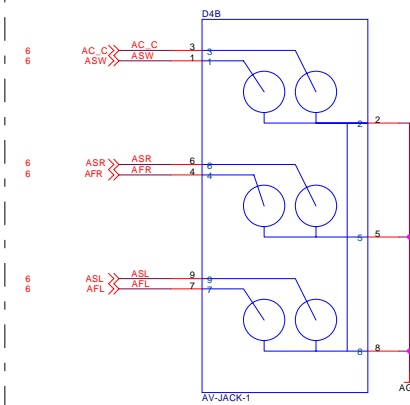
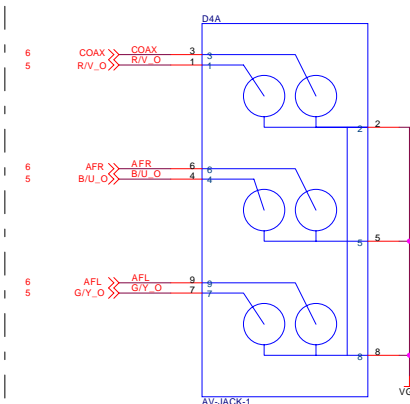
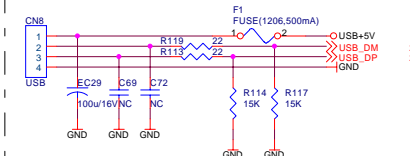
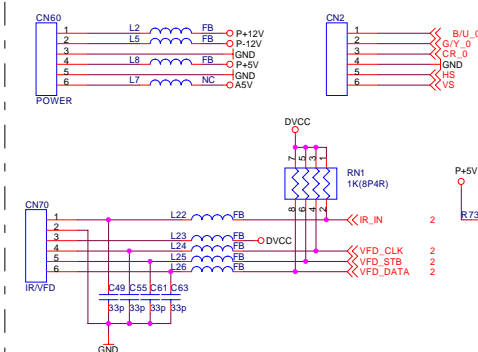
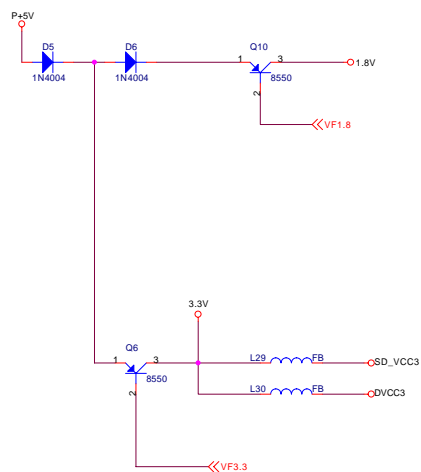
PLL +3.3V
(PLL bypass)

Audio PLL +3.3V
3 mA

Video DAC +3.3V

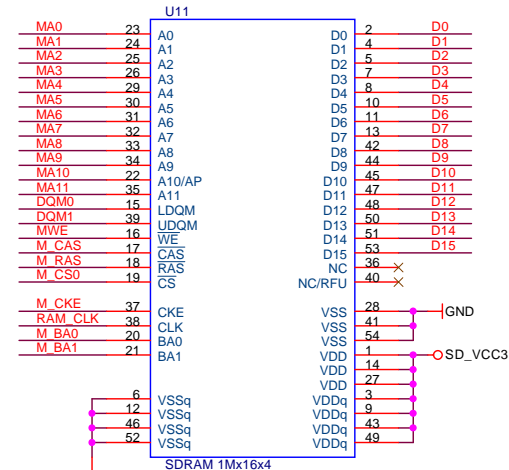
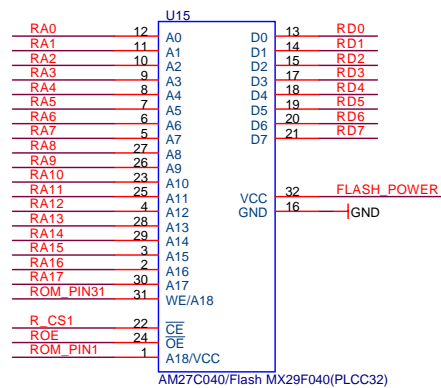
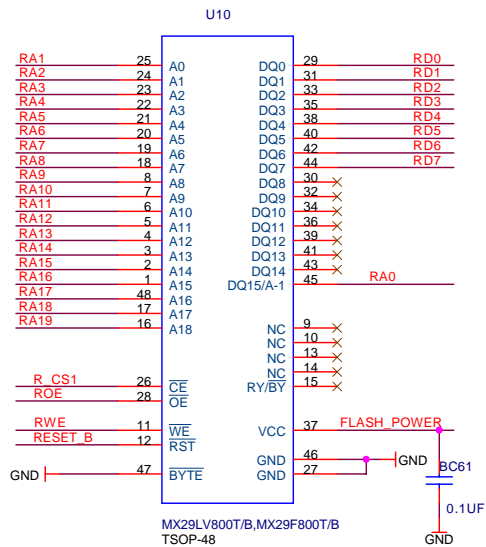


Power Bead > 2A, RAC@100 MHz
= 70 ohm, RDC(max)= 0.4 ohm



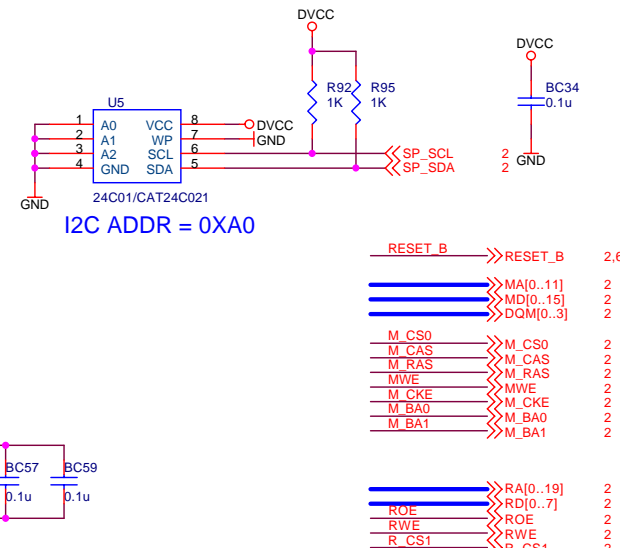
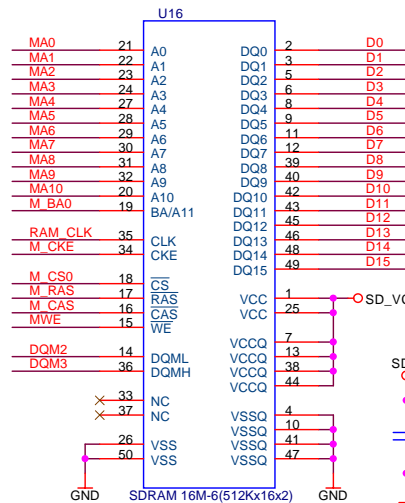
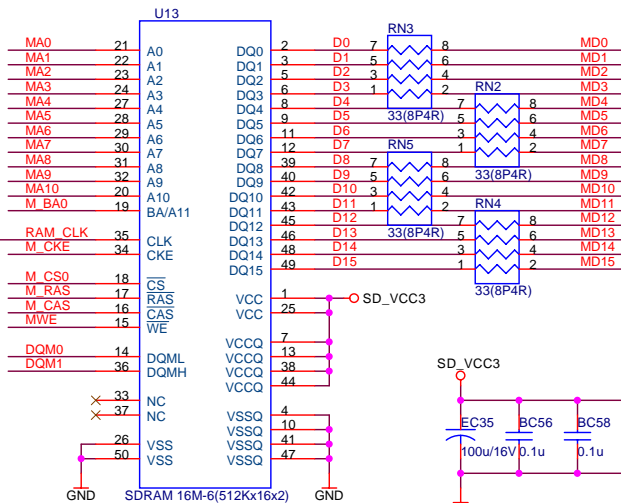
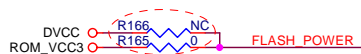


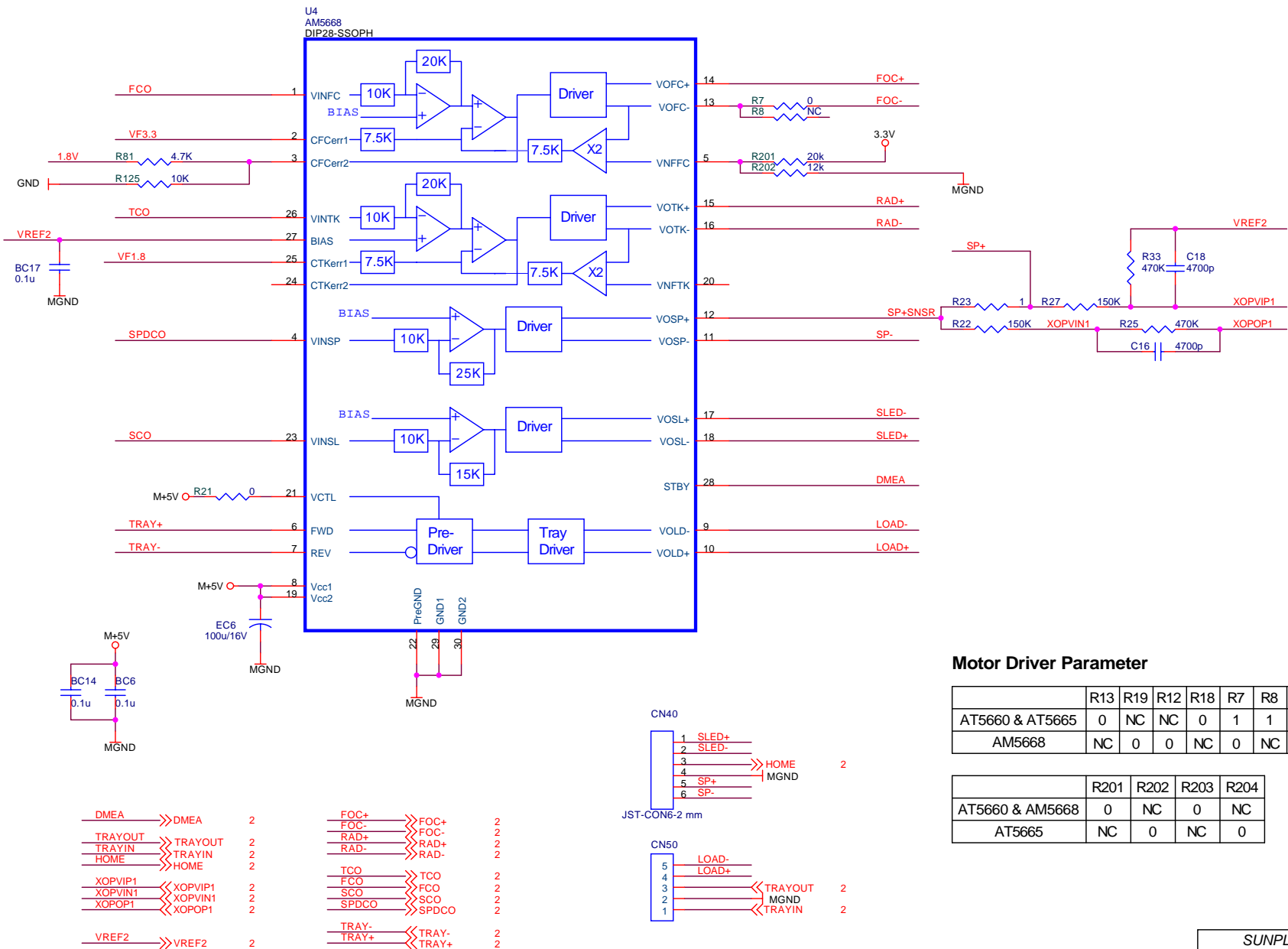




EliteMT M12L64164A-5T
 54-Pin TSOPII(400mil x 875mil)

	R165	R166	R156	R159	R148	R154	R225
040 OTP	NC	0	0	NC	NC	NC	0
080 ROM	NC	0	0	NC	0	NC	NC
FLASH	0	NC	NC	0	NC	0	NC



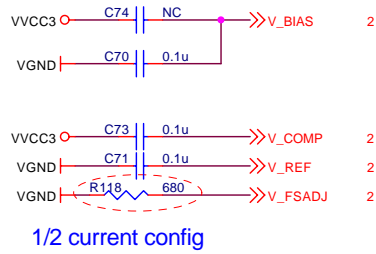
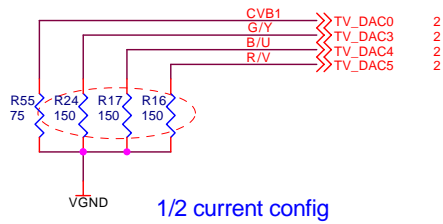


Motor Driver Parameter

	R13	R19	R12	R18	R7	R8	R4	R6	R30	R32	C15	C17	R261
AT5660 & AT5665	0	NC	NC	0	1	1	1	1	22K	22K	330p	330p	0
AM5668	NC	0	0	NC	0	NC	0	NC	NC	NC	NC	NC	NC

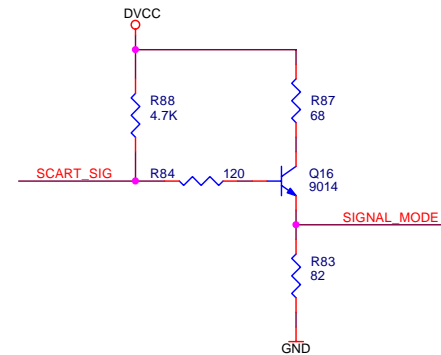
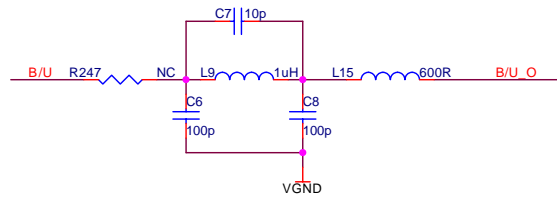
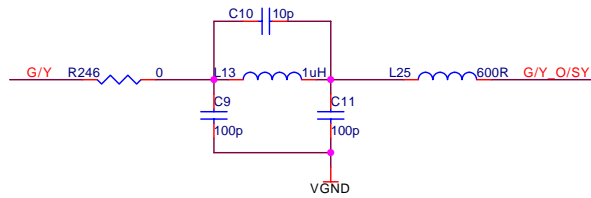
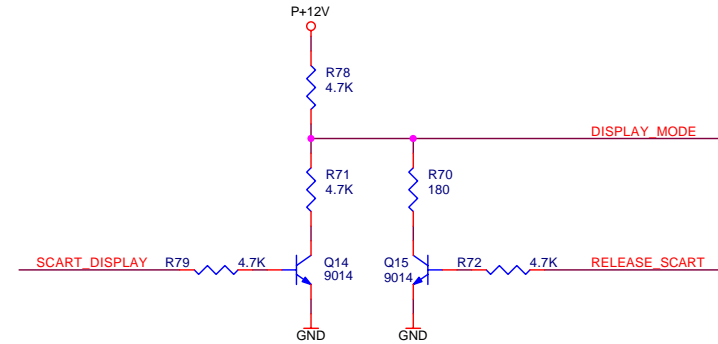
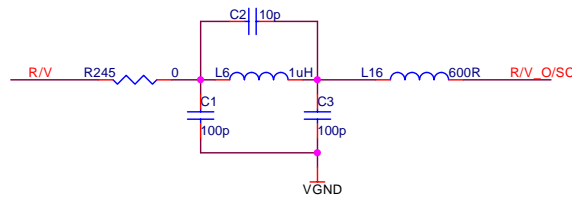
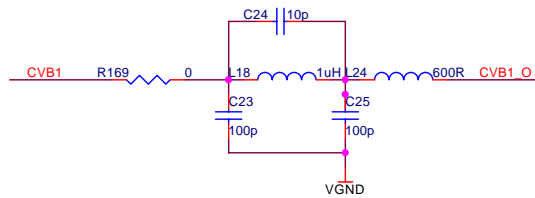
	R201	R202	R203	R204
AT5660 & AM5668	0	NC	0	NC
AT5665	NC	0	NC	0

SUNPLUS TECHNOLOGY			
Title			
Motor-Driver			
Size	Document Number		
B	8281D-16-SY-216-0-F		
Date:	Tuesday, January 10, 2006		
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TV0	TV3	TV4	TV5
CVB	Y	Cb	Cr
CVB	Y(S-Video)	CVB	C(S-Video)
CVB	G	B	R

VIDEO DAC	R118	R169	R245	R246	R247	R16	R17	R24
Full Current	390	0	0	0	0	75	75	75
Half Current	680	NC	NC	NC	NC	150	150	150



RELEASE_SCART	DISPLAY_MODE
0	Not RELEASE
1	RELEASE

SCART_DISPLAY	DISPLAY_MODE
0	12V(9.5V--12V 4:3)
1	6V(5V--8V 16:9)

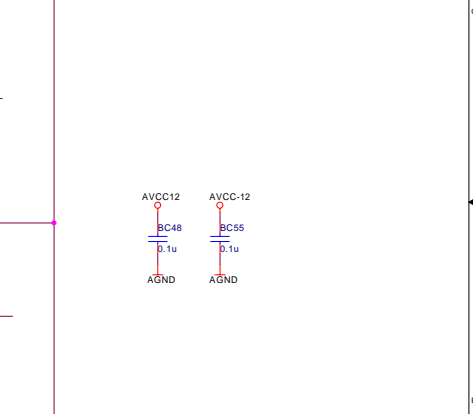
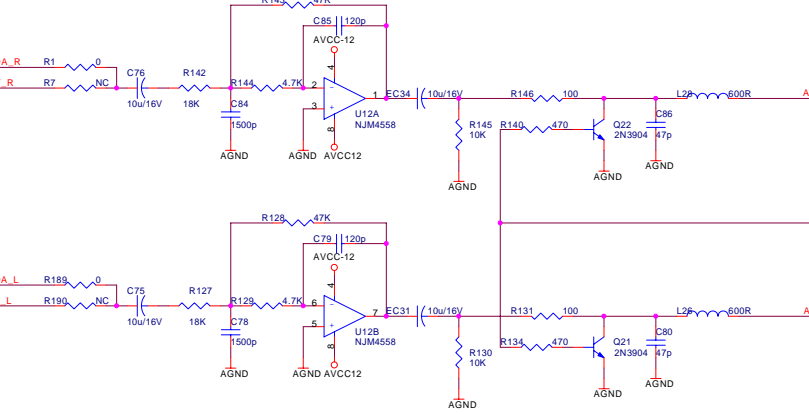
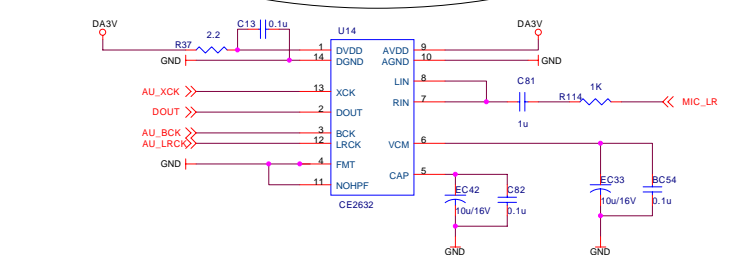
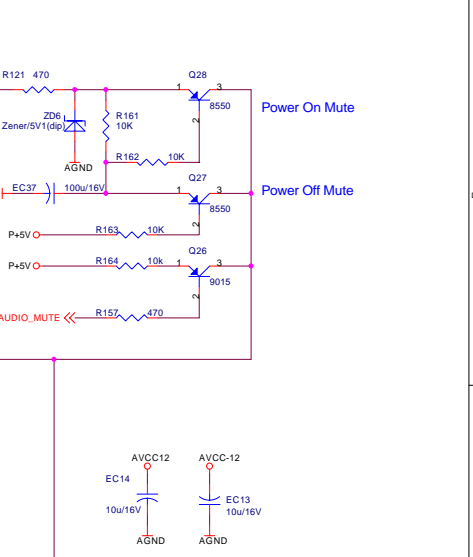
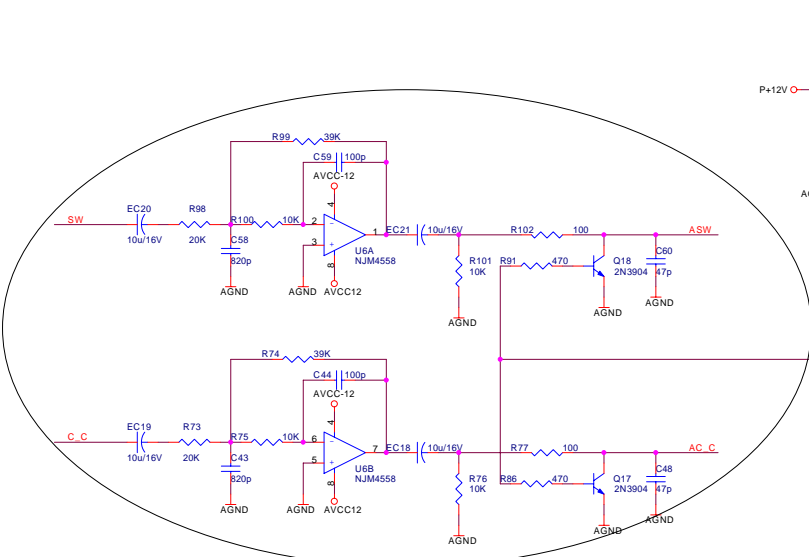
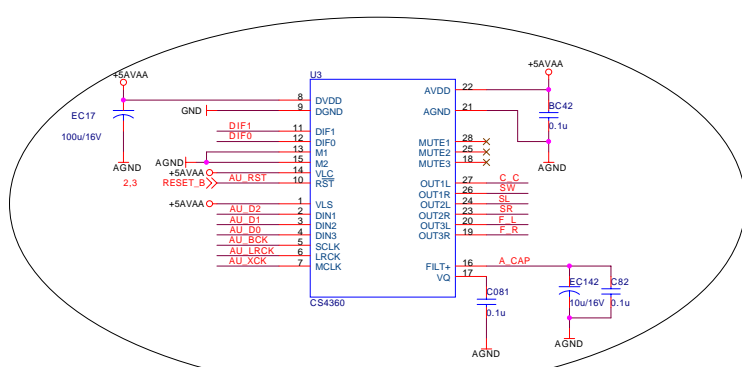
SCART_SIG	SIGNAL_MODE
0	0V(CVBS)
1	3V(RGB)

SCART_SIG << SCART_SIG 2
SCART_DISPLAY << SCART_DISPLAY 2
RELEASE_SCART << RELEASE_SCART 2

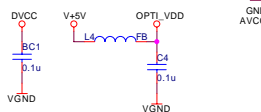
SIGNAL_MODE << SIG_MODE 1
DISPLAY_MODE << DIS_MODE 1

CVB1_O << CVB1_O 1
G/Y_O/SY << G/Y_O 1
Y_O << Y_O 1
R/V_O/SC << R/V_O 1
Cr_O << Cr_O 1
B/U_O << B/U_O 1

SUNPLUS TECHNOLOGY		
Title	Video Buffer	
Size	Document Number	Rev
B	8281D-16-SY-216-0-F	1.05
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Replace GND with VGND
For Himage mechanism



CS4360 Format

DIF1	DIF0	AUDIO INTERFACE
0	0	LJ 24 bits
0	1	I2S
1	0	RJ 16bits
1	1	RJ 24 bits

Internal DAC / External DAC setting (Default: Internal DAC)

	R127	R128	R129	C78	C79	R142	R143	R144	C84	C85	R187	R188	R189	R190
Internal DAC	18K	47K	4.7K	1500P	120P	18K	47K	4.7K	1500P	120P	0	NC	0	NC
External DAC	20K	39K	10K	820P	100P	20K	39K	10K	820P	100P	NC	0	NC	0

